GECA/E&TC/Store/2019-20/ 569

Date. 06.02.2020   17 FEB 2020

To

GECA Notice Board & Website

Subject :- Quotation for Supply of following EDGE ZYNQ SoC FPGA Development Board as per details given below

Dear Sir,

You are requested to send your competitive quotations for the supply of the following items subject to the following conditions.

CONDITIONS:

1 Rates quoted should be FOR AURANGABAD or free delivery at the Institute inclusive of all lead and Lift.

2 Detailed specifications of the articles you intend to supply should be given. If not according to the specification, laid down here under.

3. The material should be supplied within (04) Weeks from the date of order. List of material is given below.

4. The earliest delivery period should be quoted if you cannot supply within the period mentioned above.

5. Quotation should be in sealed cover and superscripted as **"Quotations" for E&TC, Department** Due on : 13-02 -2020 at 5 P.M.

6. Quotation should be valid for Six Month.

7. Quotation not complying with the above conditions and incomplete once will not be considered.

8. Right to reject any or all quotations rates are with the under signed.

9. No advance shall be paid and No part payment shall be made.

10. Rates quoted must be inclusive of All applicable Taxes.

11. Contractor should submit his authorized registration of contractor ship.

12. He has work experience with Government institution

13. Order will be finalized based on overall value of items

Sr. No.	Specification	Approx Qty.	Remark
01	**EDGE ZYNQ SoC FPGA Development Board**	10	Details specification Annexure attached

Head ,
E&TC Department.
Govt. College of Engineering,
Aurangabad

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Technical Specification for EDGE ZYNQ SoC FPGA Development Board
Description

EDGE ZYNQ SoC FPGA Development Board is a feature rich and high-performance Single Board Computer built around the Xilinx Zynq-7010 (XC7Z010). It features integrated dual-core ARM Cortex-A9 processor with Xilinx 7-series FPGA.

EDGE ZYNQ SoC FPGA Development Board is designed to create best learning experience of both processing system (PS) and programming logic(PL). It features Xilinx Z-7010 SoC, 512MB DDR3 SDRAM and 16MB QSPI Flash USB-to-UART, USB OTG, 10/100/1000Mbps Ethernet, HDMI, USB JTAG, Temperature sensor, Micro SD, WiFi, Bluetooth, ADC, LCD, 7 Segment, camera, TFT, Buzzer, Switches, buttons and LEDs.

Advantage of EDGE FPGA kit is easy to implement plenty of applications ranging from single board computer, Wireless control, Image/video Processing, Internet of Things without additional interfaces.

Xilinx offers free WebPACK™ versions of Vivado design suite, EDGE ZYNQ SoC FPGA Development Board consist of

- Xilinx Zynq XC7Z010 FPGA
- 512MB DDR3
- 16MB QSPI Flash
- On-Board USB JTAG Programmer
- USB to UART Interface
- 10/100/1000M Ethernet
- USB OTG
- Micro SD
- WiFi 802.11 b/g/n
- Bluetooth 4.2 and BLE
- 12 bit VGA Interface
- HDMI Tx/Rx
- ADC 2 channel
- Temperature Sensor
- LDR Interface
- Stereo Audio Jack
- 2x16 LCD Display
- 4 Digit Seven Segment Display
- 50 MHz and 33 MHz Clock
- 4 Slide switches
- 2 PL and 1 PS Push Button
- 5v Buzzer
- 4 PL and 1 PS LED
- 31 PL and 4 PS I/O

EDGE ZYNQ SoC FPGA Development Board		Cost per Unit	Qty
FPGA	Xilinx XC7Z010-1CLG400C (Zynq-7010) – 667MHz ARM® dual-core Cortex™-A9 processor – Integrated Artix-7 FPGA with 28K logic cells, 17,600 LUTs, 80 DSP slices – NEON™ & Single / Double Precision Floating Point for each processor – Supports a Variety of Static and Dynamic Memory Interfaces	20250 Rs	10
DDR3	512MB DDR3 SDRAM (16-bit)		
SPI FLASH	16MB QSPI Flash		
Wireless Communication	Single Chip Wireless Module – Wi-Fi 802.11b/g/n – Bluetooth: v4.2 BR/EDR and BLE (Bluetooth Low Energy)		
Gigabit Ethernet	10/100/1000M Ethernet		
USB2.0 OTG	USB OTG (Device/Host/OTG)		
VGA	12-bit, 4096 Color		
Camera	OVi 5 Megapixel camera		
TFT	1.8 inch SPI TFT, 128×160		
HDMI	HDMI TX/RX		
Sensor	LM35 Temperature Sensor LDR		
PS I/O	Support external interfaces like SPI, I2C, UART, CAN on PL side		
Audio	Stereo Jack Audio Output		
7 segment Display	4-Digit 7 Segment Display		
LCD	2×16 Character LCD		
Buzzer	5v Piezo Buzzer		
Slide Switch	4 SPDT Slide switch on PL Side		
LEDs	4 LED on PL side 1 LED on PS side		

Push Button	2 Push Button on PL side 1 Push Button on PS side 1 Push Button for Reset	
MicroSD	MicroSD card up to 128 GB support	
JTAG	USB JTAG for Programming and Debugging. Fully Compatible with Xilinx Vivado Tool.	
UART	USB UART communicate at different baud rate	
Clock	50 MHz Clock on PL side 33.33 MHz Clock on PS side	